



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants: Boucher, et al.
Assignee: Alacritech, Inc.
Title: "Transmit Fast-Path Processing On TCP/IP Offload Network Interface Device"
Serial No.: 10/023,240 Filed: December 17, 2001
Examiner: Mehmet Geckil Tel: 571-272-3894
Docket No.: ALA-006A Art Unit: 2142

May 9, 2005

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313

INFORMATION DISCLOSURE STATEMENT

Sir:

Examiner Geckil returned an eight page form PTO-1449 to Applicants along with a Supplemental Notice of Allowability dated April 14, 2005. When Applicants submitted the forms PTO-1449 along with the original application papers, Applicants made a bona fide attempt to comply with 37 C.F.R. 1.98 under the rules then pending.

Certain of the documents listed on the form PTO-1449 received back with the Supplemental Notice of Allowability were crossed out. The undersigned telephoned Examiner Geckil May 28, 2005 and inquired about why the documents were crossed out. Examiner Geckil stated that the documents were not "available" to him. Examiner Geckil recommended that Applicants resubmit copies of the documents "officially." Examiner Geckil stated that if the Patent Office scans in the documents such that they are "available", that he would then review the documents.

Examiner Geckil is thanked for his guidance, service and understanding of the situation.

Applicants therefore submit the enclosed 44 documents along with forms PTO-1449 (3 pages) listing the enclosed documents. Also enclosed is a Compact Disk containing electronic copies of the documents. The undersigned can be reached at (925) 485-9923.

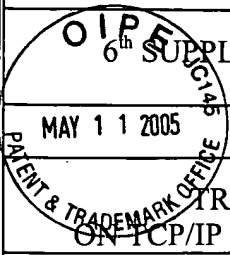
I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on May 9, 2005.

May 9, 2005 T. Lester Wallace
Date T. Lester Wallace

Respectfully submitted,

T. Lester Wallace

T. Lester Wallace
Attorney for Applicants
Reg. No. 34,748

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/023,240
	Filing date: December 17, 2001
 SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Boucher, et al.
	Group Art Unit: 2142
TRANSMIT FAST-PATH PROCESSING ON TCP/IP OFFLOAD NETWORK INTERFACE DEVICE	Examiner name: Mehmet B. Geckil
	Attorney Docket No. ALA-006A

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	1	WO 98/50852 (PCT/US98/08719)	11/12/98	International				
	2	WO 98/19412 (PCT/US97/17257)	5/7/98	International				
	3	WO 99/04343 (PCT/US98/14729)	1/28/99	International				
	4	WO 00/13091 (PCT/US98/24943)	3/9/00	International				
	5	WO 01/40960 A1 (PCT/US00/32660)	6/7/01	International				
	6	WO 99/65219 (PCT/US99/13184)	12/16/99	International				
	7	WO 01/05107 A1 (PCT/US00/19006)	7/13/00	International				
	8	WO 01/05116 A2 (PCT/US00/19243)	7/13/00	International				
	9	WO 01/05123 A1 (PCT/US00/18976)	7/12/00	International				
	10	WO 01/04770 A2 (PCT/US00/18939)	7/11/00	International				

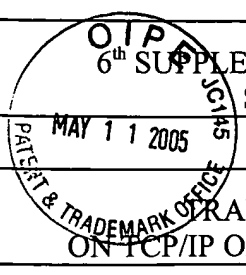
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

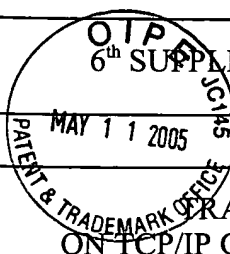
11	Internet pages entitled: DART Fast Application – Level Networking Via Data-Copy Avoidance, by Robert J. Walsh, 24 pages, printed 6/3/99.
12	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
13	Internet pages entitled: Technical White Paper – Xpoint's Disk-to-LAN Acceleration Solution for Windows NT Server, printed 6/5/97.
14	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slide presentation, 12 pages, printed 8/19/98.
15	EETIMES article entitled: Enterprise System Uses Flexible Spec, by Christopher Harrer and Pauline Shulman, dated August 10, 1998, Issue 1020, 3 pages, printed 11/25/98.
16	Internet pages from website http://www.ireadyco.com/about.html , 3 pages, downloaded 11/25/98.

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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		Attorney Docket No. ALA-006A
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
17	Internet pages entitled: Smart Ethernet Network Interface Card, which Berend Ozceri is developing, 2 pages, printed 11/25/98.	
18	Internet pages entitled: Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, 1 page, printed 11/25/98.	
19	Internet pages of XaQti Corporation entitled: Giga POWER Protocol Processor Product Preview, 4 pages, printed 11/25/98.	
20	Internet pages of Xpoint Technologies www.xpoint-tech.com web site (5 pages), printed 12/19/97.	
21	Internet pages entitled "iReady Products" from website http://www.ireadyco.com/products.html , 2 pages, printed 11/25/98.	
22	Internet pages entitled: "Asante and 100BASE-T Fast Ethernet", from website www.asante.com , printed 5/27/97.	
23	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, printed 12/21/98.	
24	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).	
25	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, revision 1.3 (Feb. 1, 2000).	
26	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.	
27	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.	
28	Internet pages directed to; Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com , 14 pages, printed 3/15/97.	
29	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter and about Adaptec's IP storage activities, 11 pages, downloaded and printed 10/1/01.	
30	Internet web pages from "iSCSIhba.com" website directed to JNI iSCSI HBAs including the "FCE-3210/6410", 10 pages, downloaded and printed 10/1/01.	
31	Internet web pages from the "iSCSI Storage.com" website that mention an Emulex HBA, 2 pages, downloaded and printed 10/1/01.	
32	The I-1000 Internet Tuner Features, iReady Corporation, 2 pages, date unknown.	
33	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).	
34	Andrew S. Tanenbaum, Computer Networks, Third Edition, ISBN 0-13-349945-6 (1996).	
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
35	Iready News Archives. Toshiba, iReady shipping Internet Chip, 1 page, printed 11/28/98.	
36	IReady News Archives, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA. 2 pages, 11/20/97, downloaded and printed 11/02/98.	
37	IReady News Archives, "Seiko Instruments Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on IReady Technology", 2 pages 10/26/98, downloaded and printed 11/2/98.	
38	NEWSwatch - IReady Internet Tuner to Web Enable Devices, November 5, 1996. 2 pages, printed 11/02/98.	
39	David Lammers, EETimes, 06/13/97, "Tuner for Toshiba, Toshiba taps iReady for Internet tuner," 2 pages, printed 11/02/98.	
40	Internet pages entitled: Comparison of Novell Netware and TCP/IP Protocol Architectures, by Janique S. Carbone, 19 pages, July 16, 1995, downloaded and printed 4/10/98.	
41	Internet web pages from the "iSCSIhba.com" website that mention QLogic HBAs including the "SANblade 2300 Series", 8 pages, downloaded and printed 10/1/01.	
42	"Two Way TCP Traffic over Rate Controlled Channels: Effects and Analysis", IEEE Transactions on Networking, Vol. 6, No. 6, pages 729-743 (December 1998).	
43	Internet pages from IReady News Archives entitled "iReady Rounding Out management team with two key executives," 2 pages, (printed 11/28/98).	
44	Toshiba, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady's Design", 3 pages, Press release 10/14/98, downloaded 11/28/98.	
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